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Fig 1

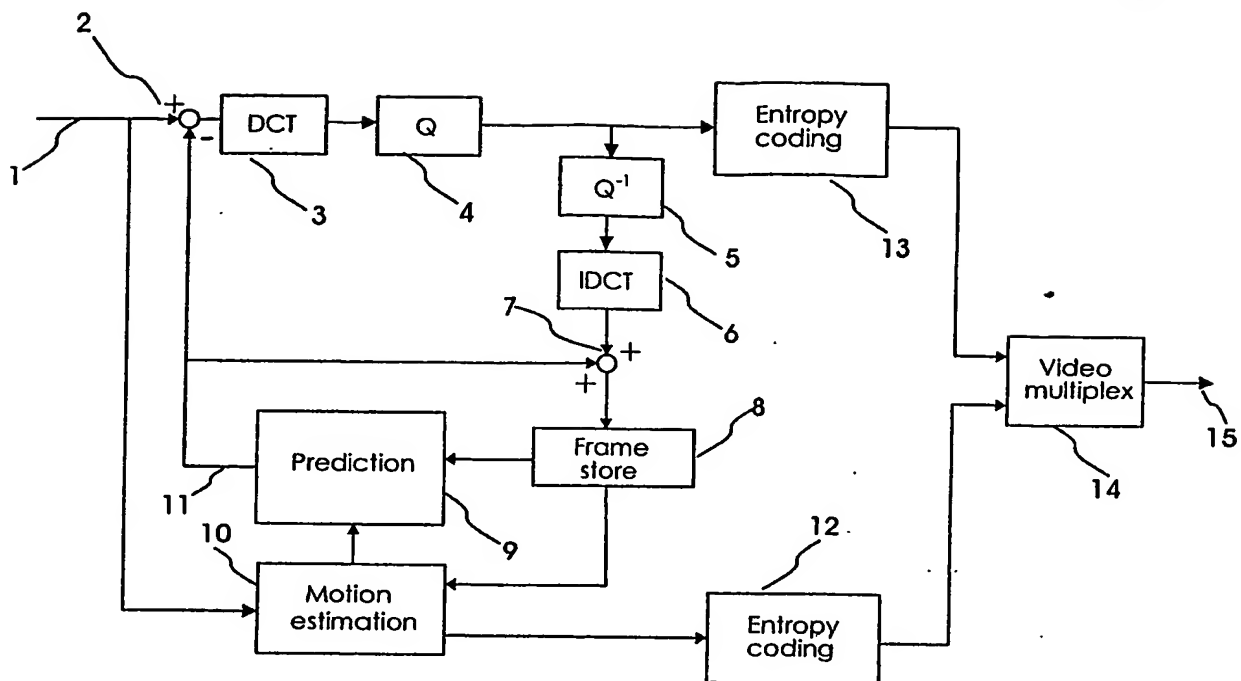
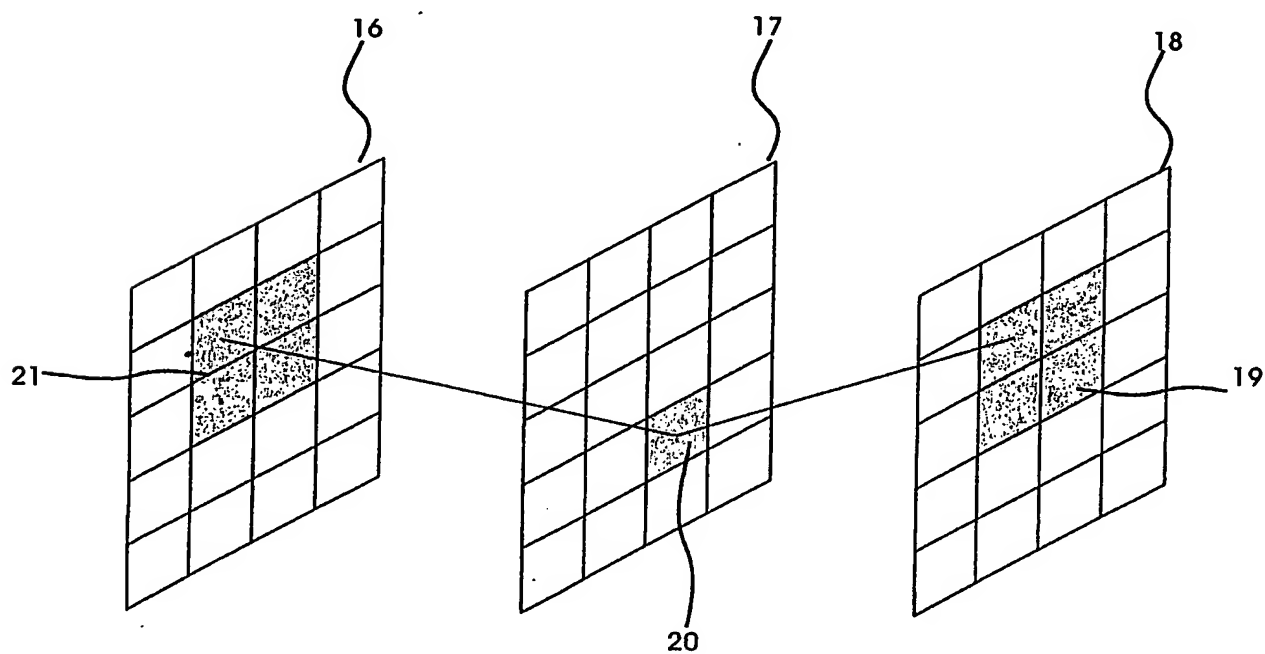


Fig 2



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Fig 3

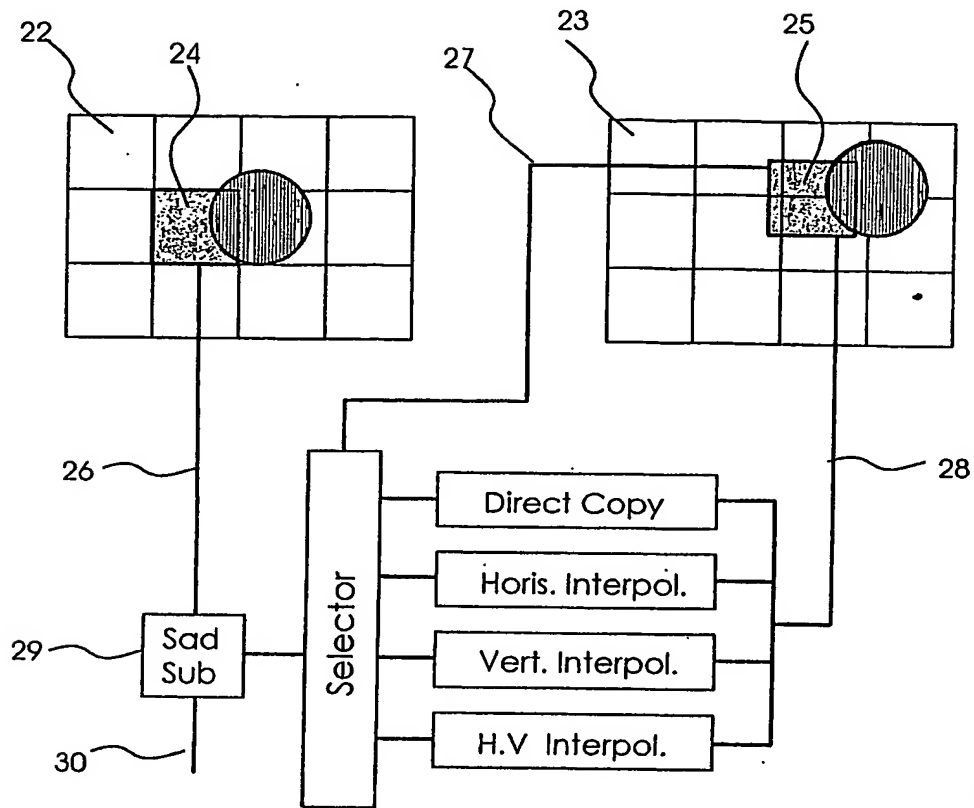
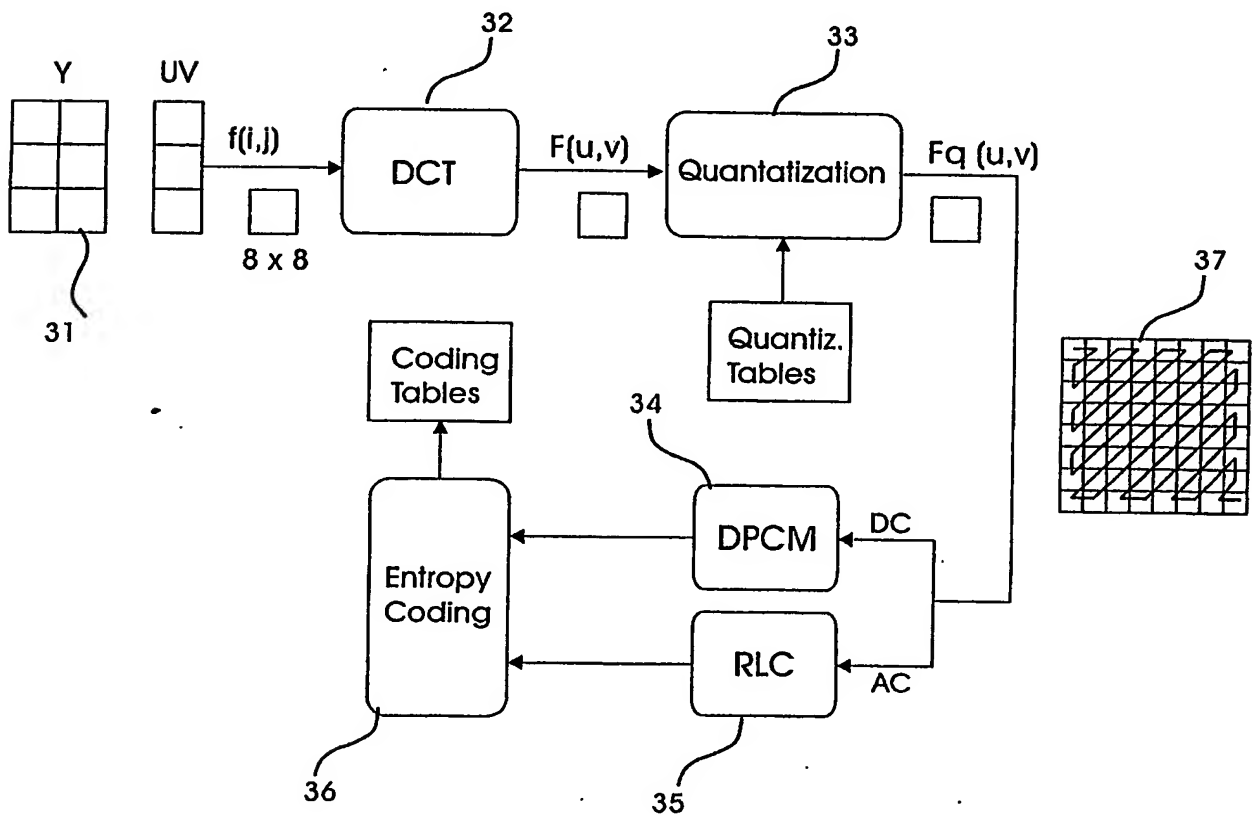


Fig 4



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Fig 5

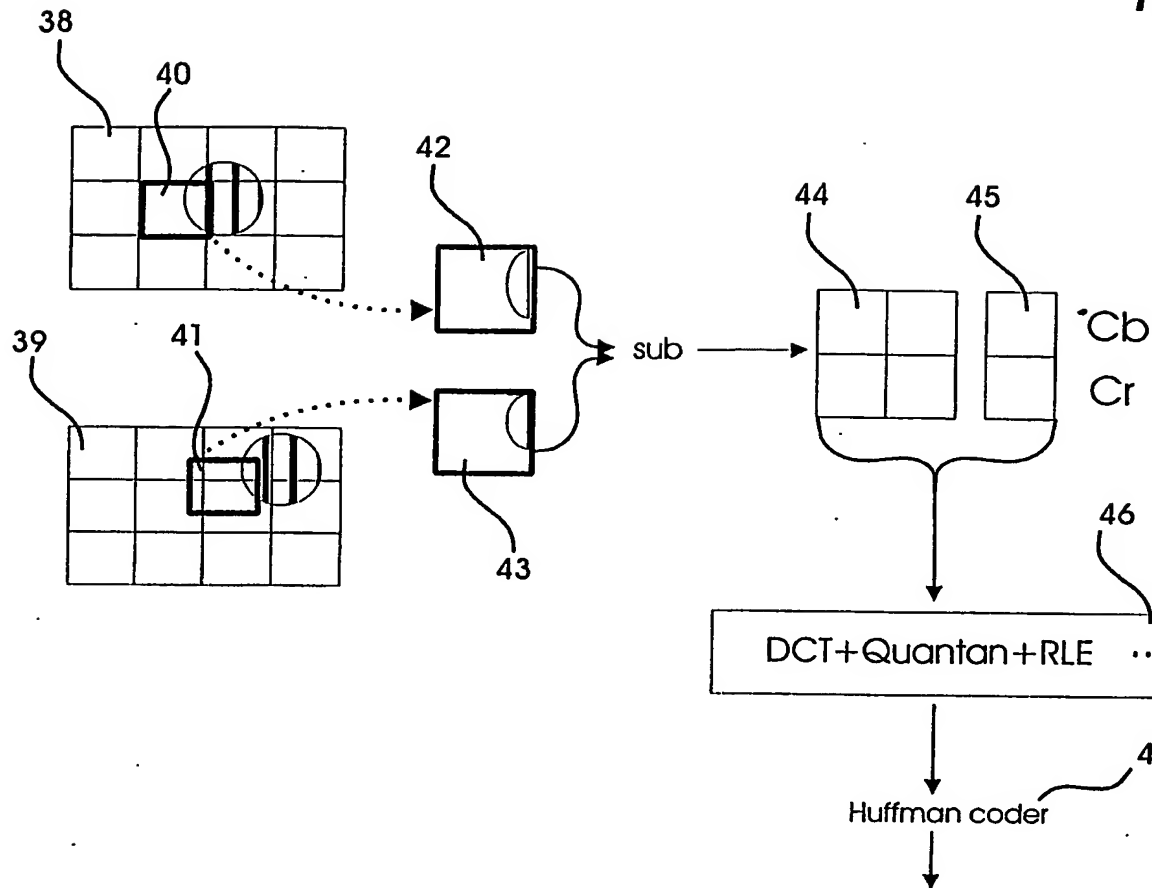


Fig 6

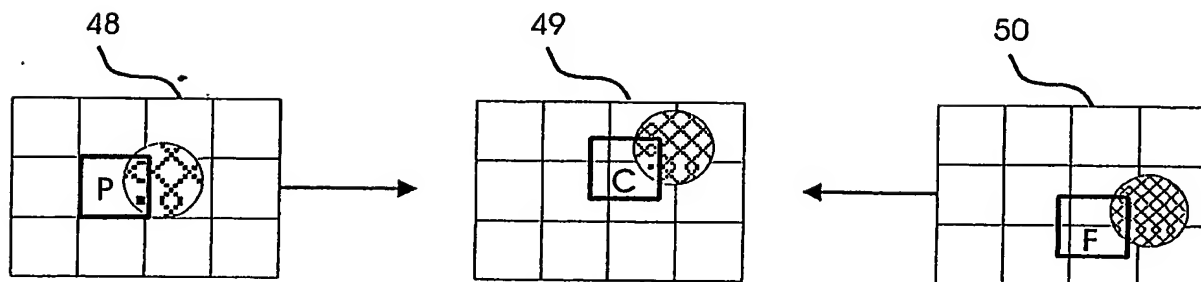


Fig 7

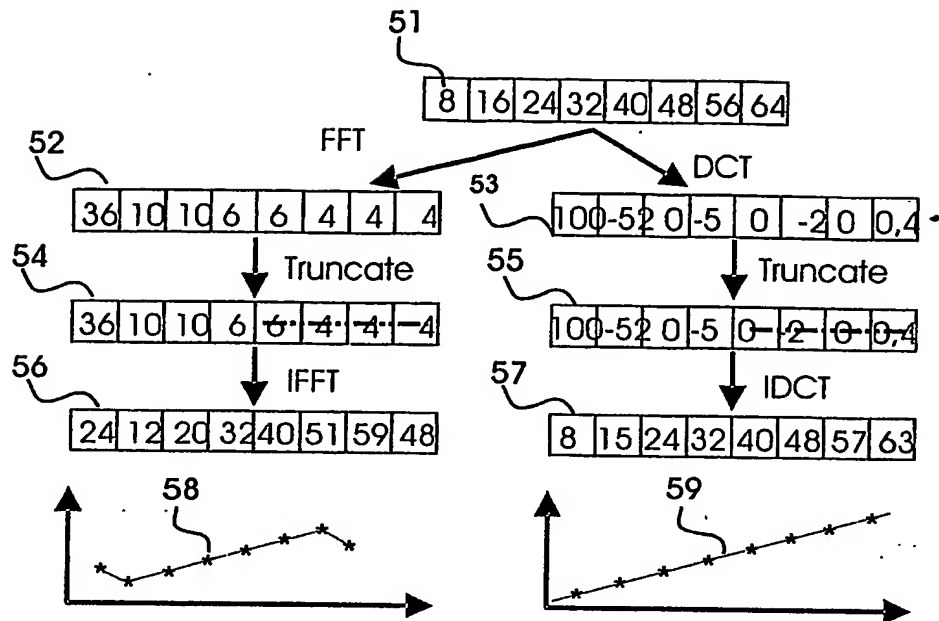
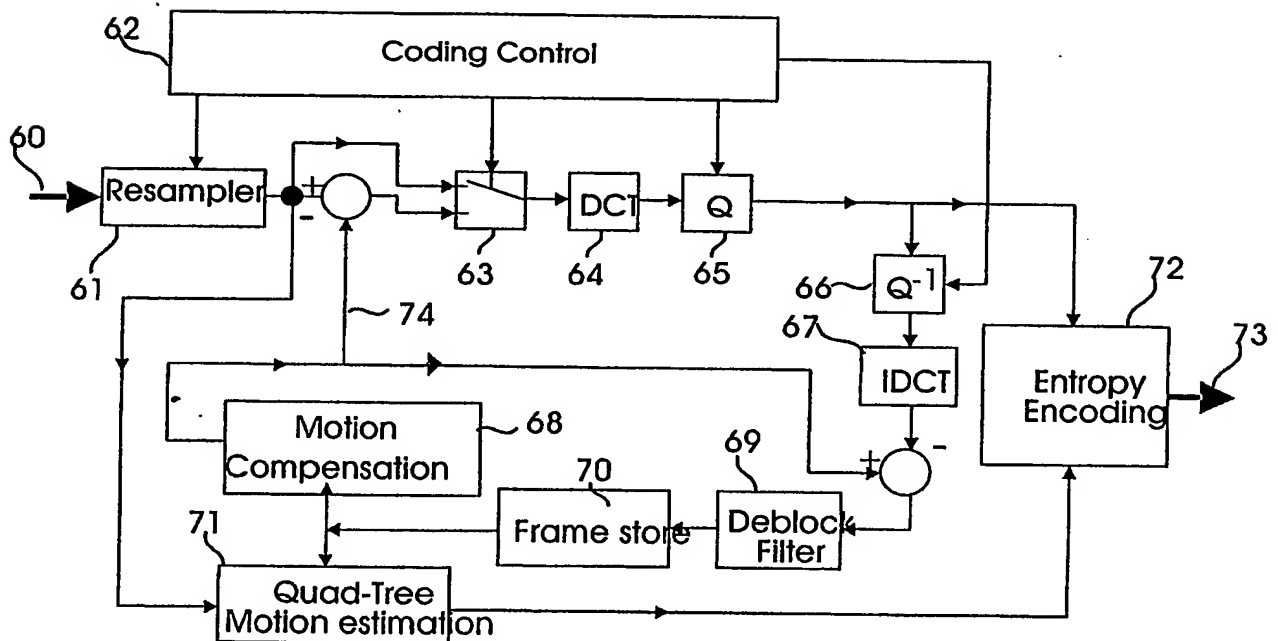


Fig 8



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Fig 9

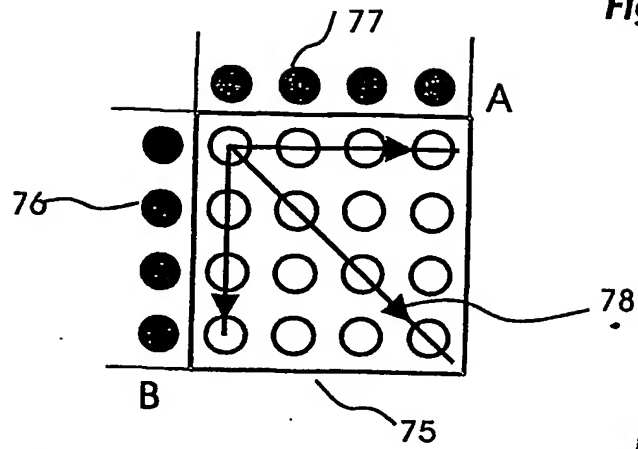


Fig 10

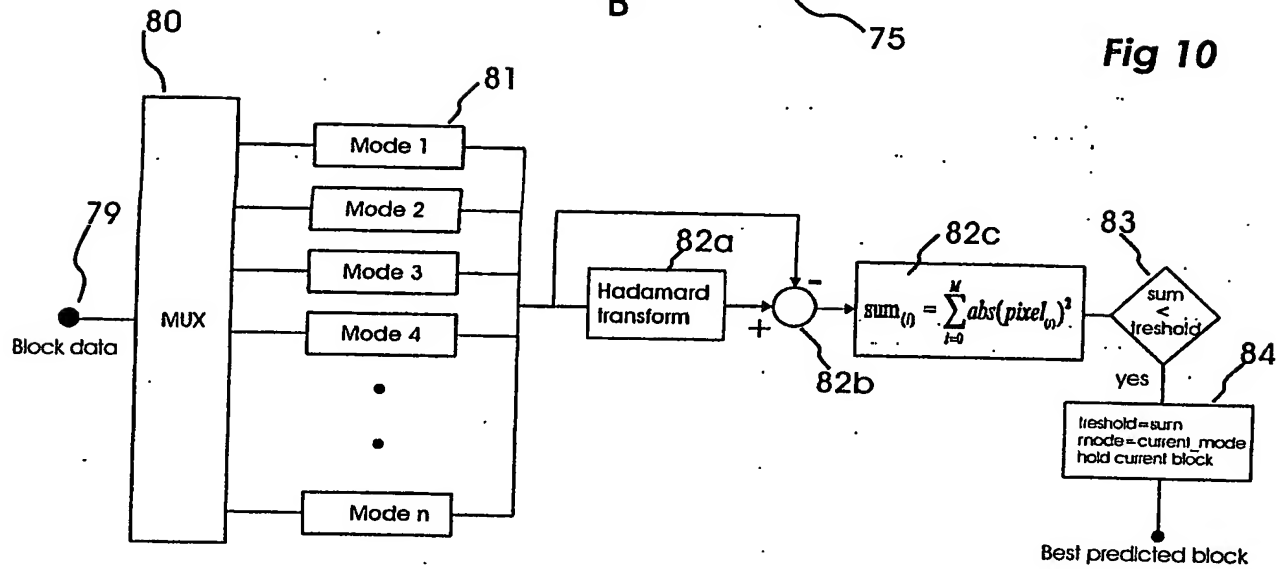
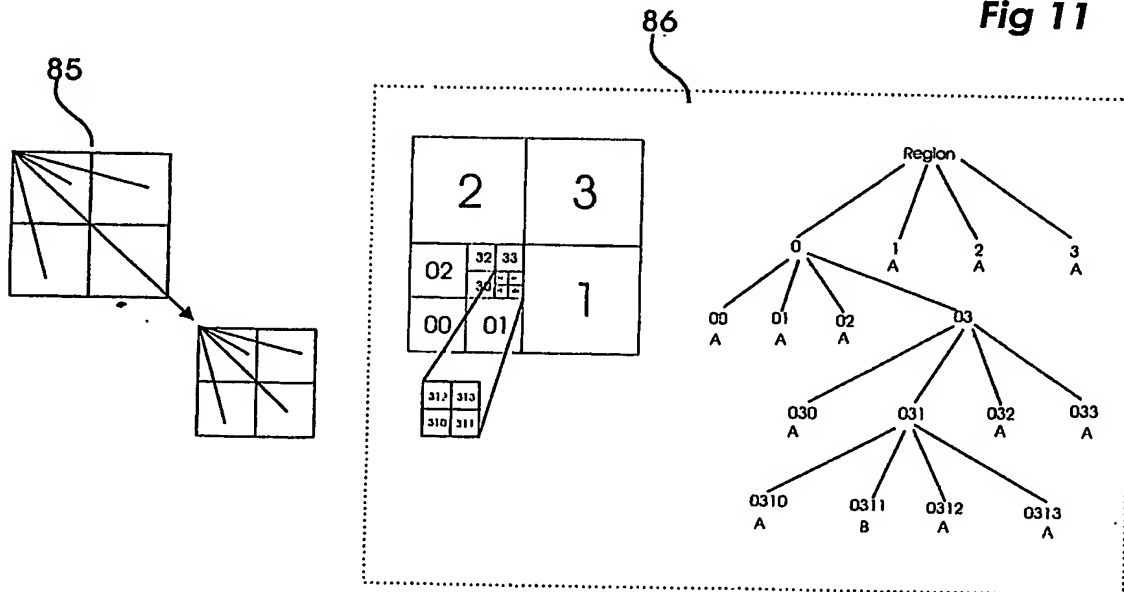


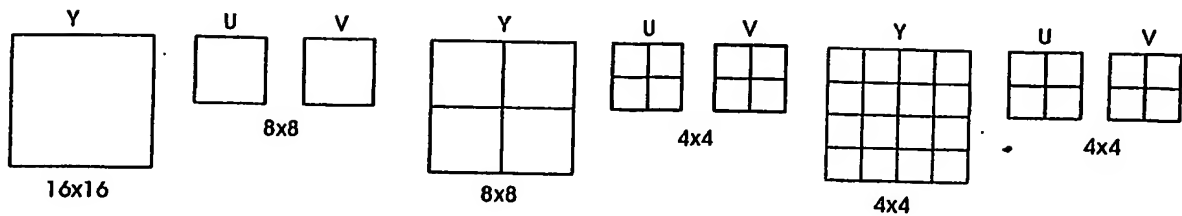
Fig 11



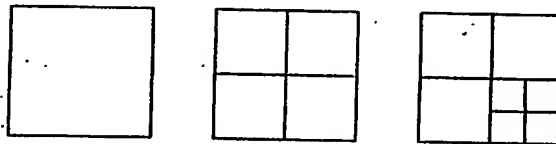
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Fig 12

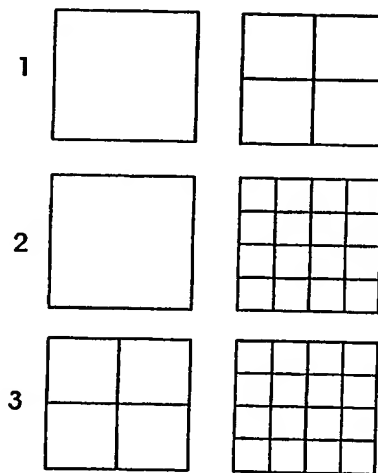
Basic block partition



Three block mode

Fig 13

Two block mode

Fig 14

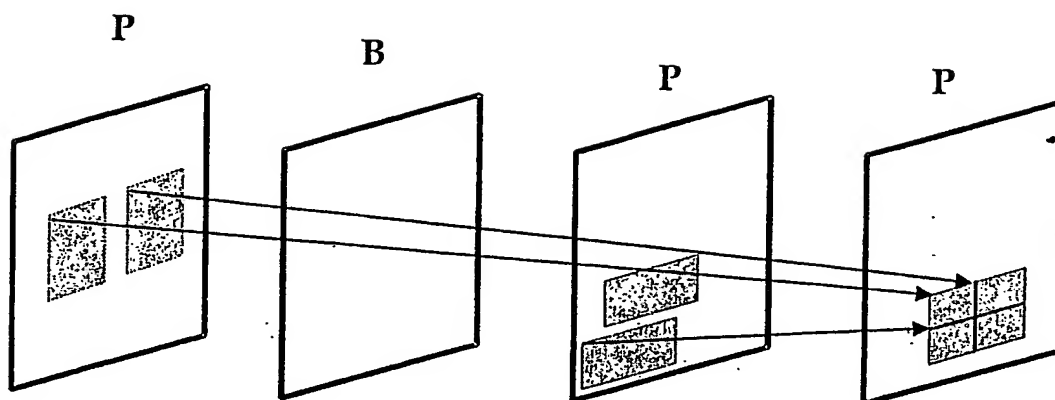


Fig. 15a

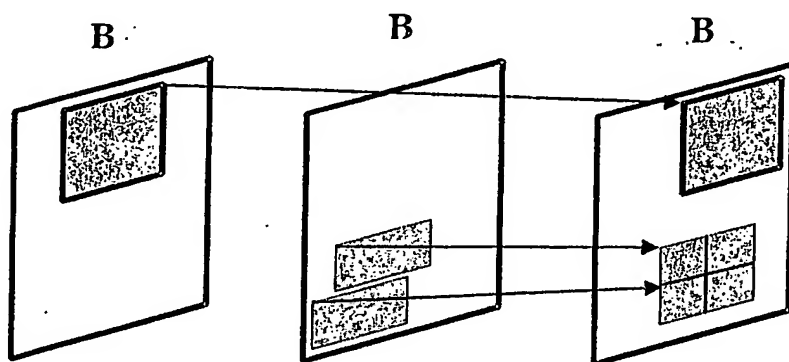


Fig. 15b

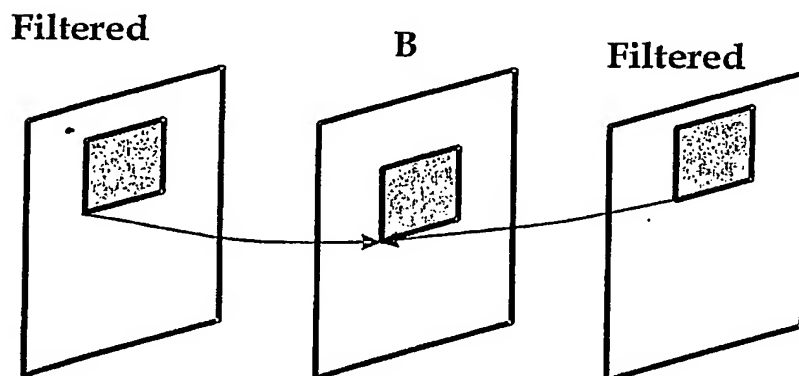
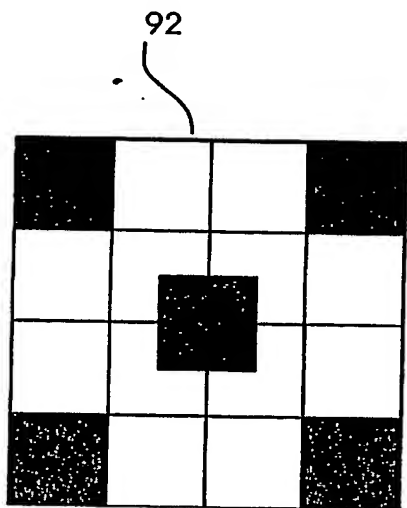
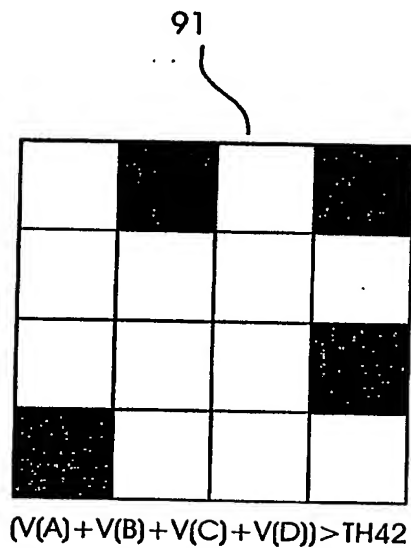
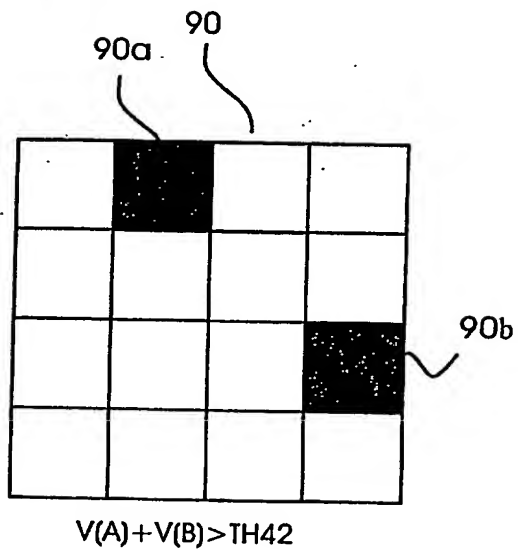
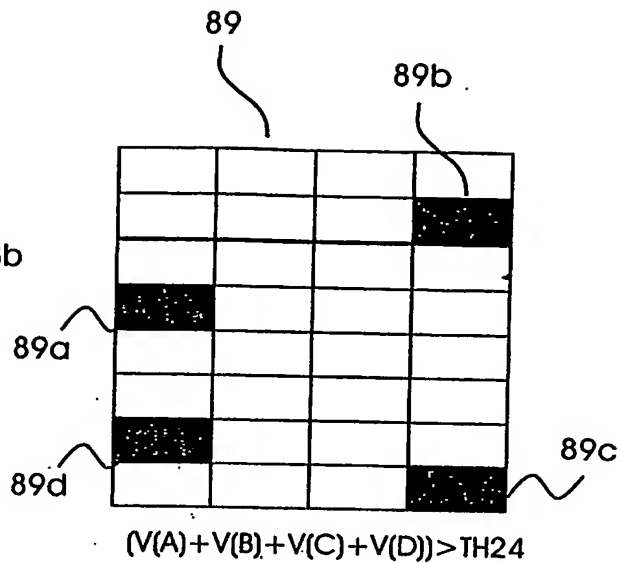
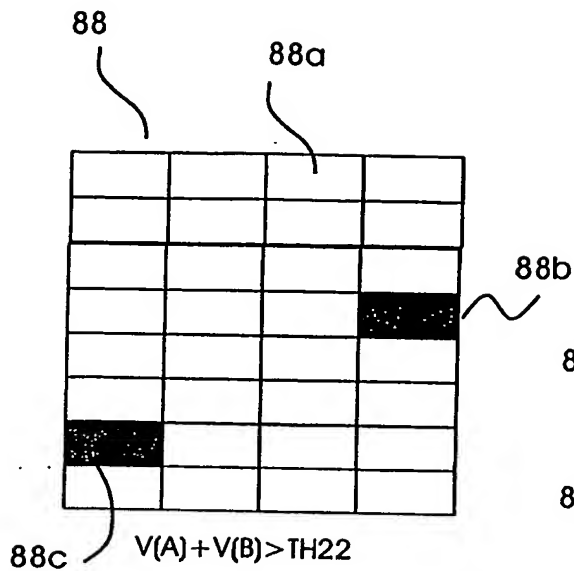


Fig. 15c

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Fig 16



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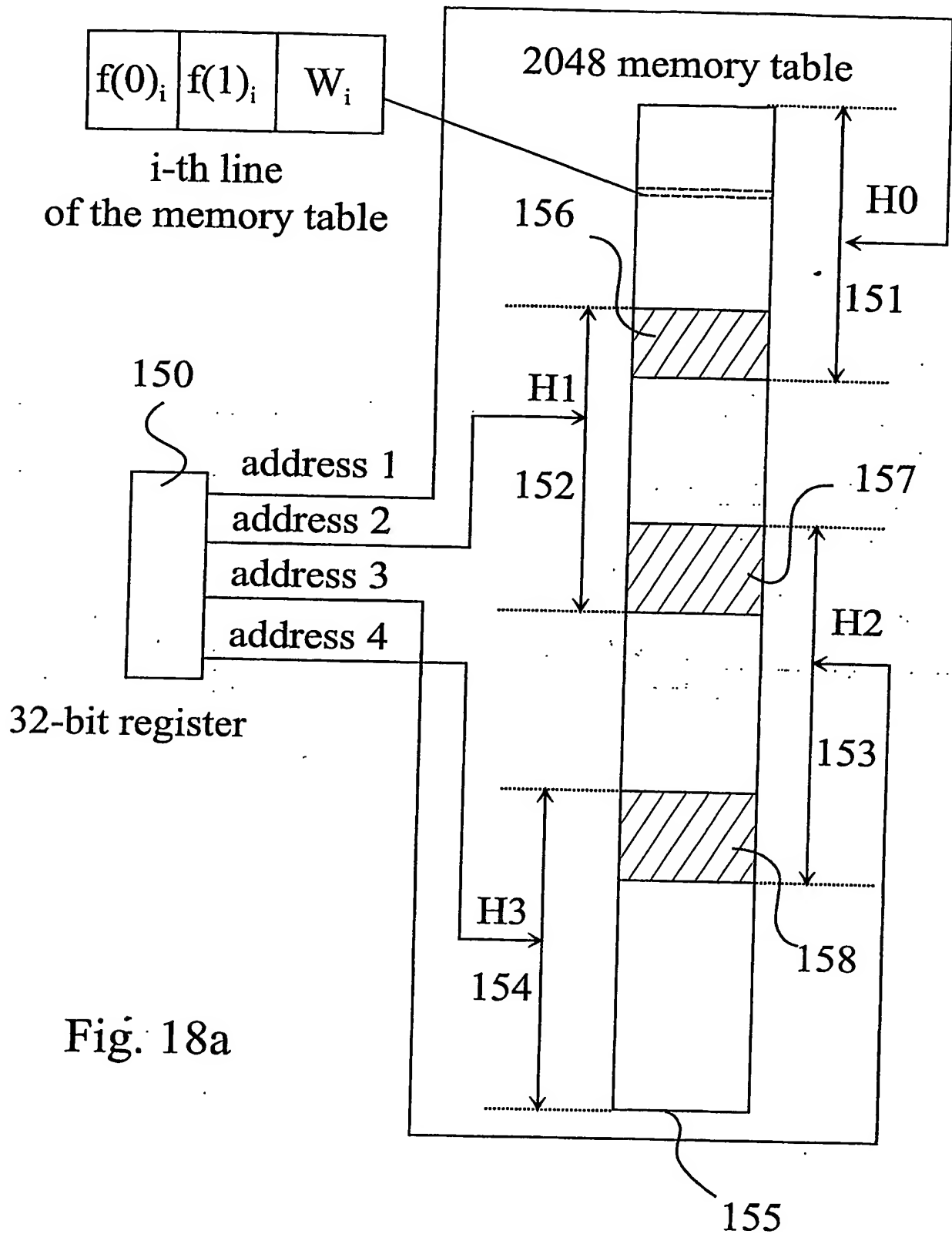
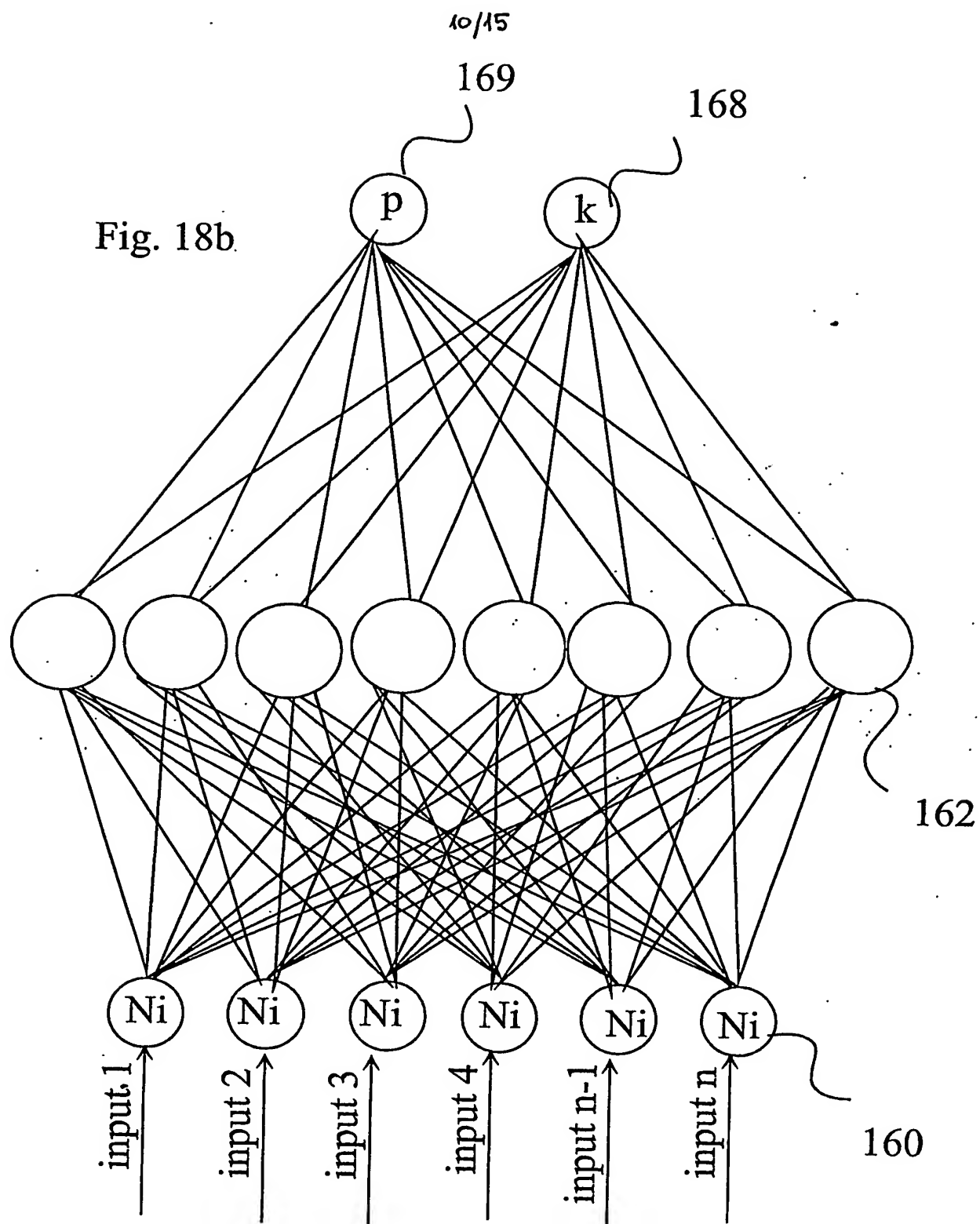


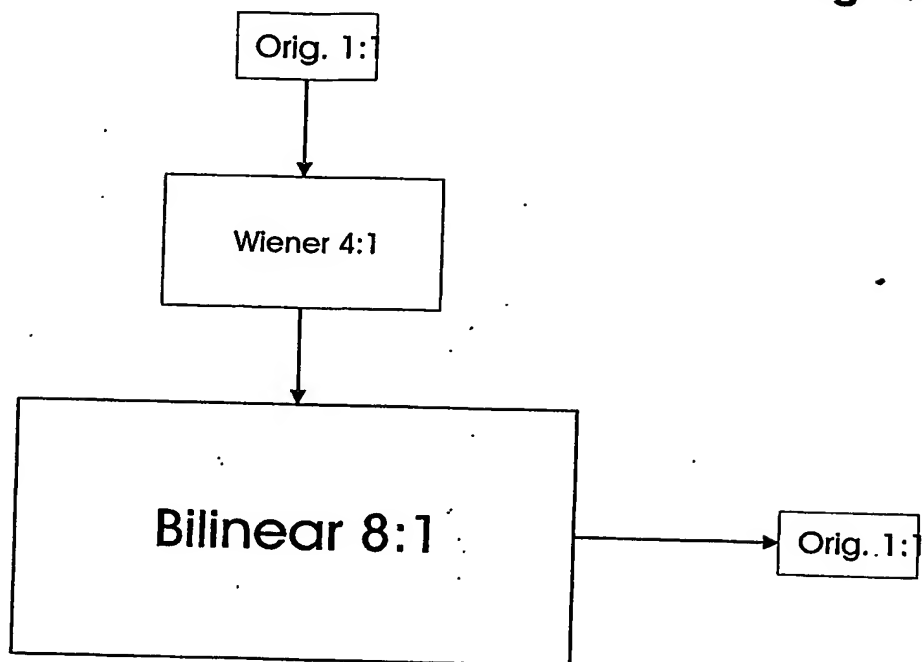
Fig. 18a



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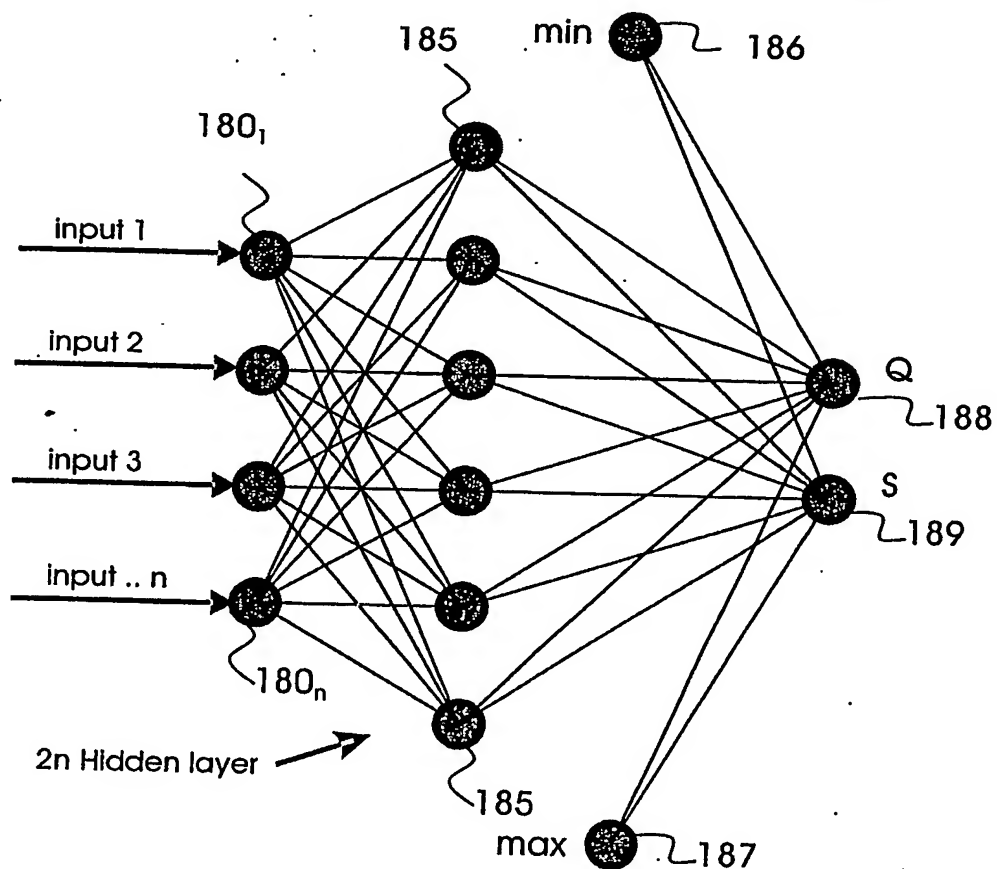
Reference image creation

Fig 17



Back propagation

Fig 18c



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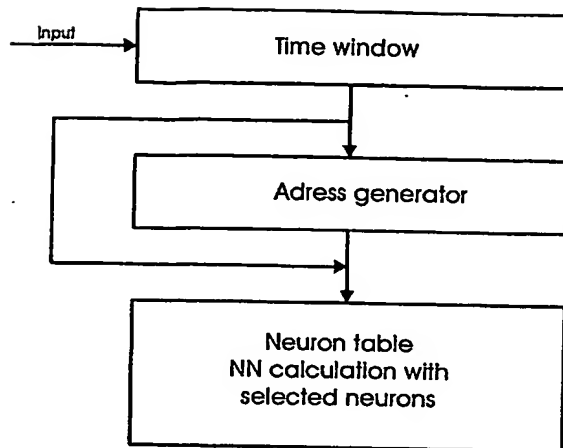


Fig 19

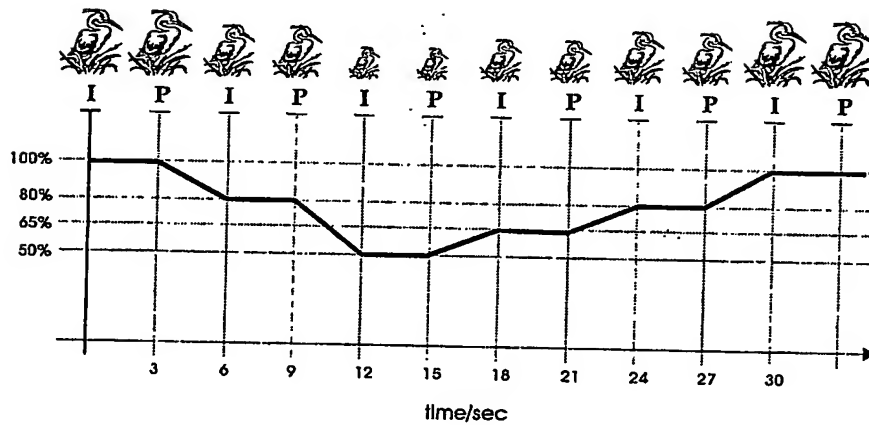


Fig 20

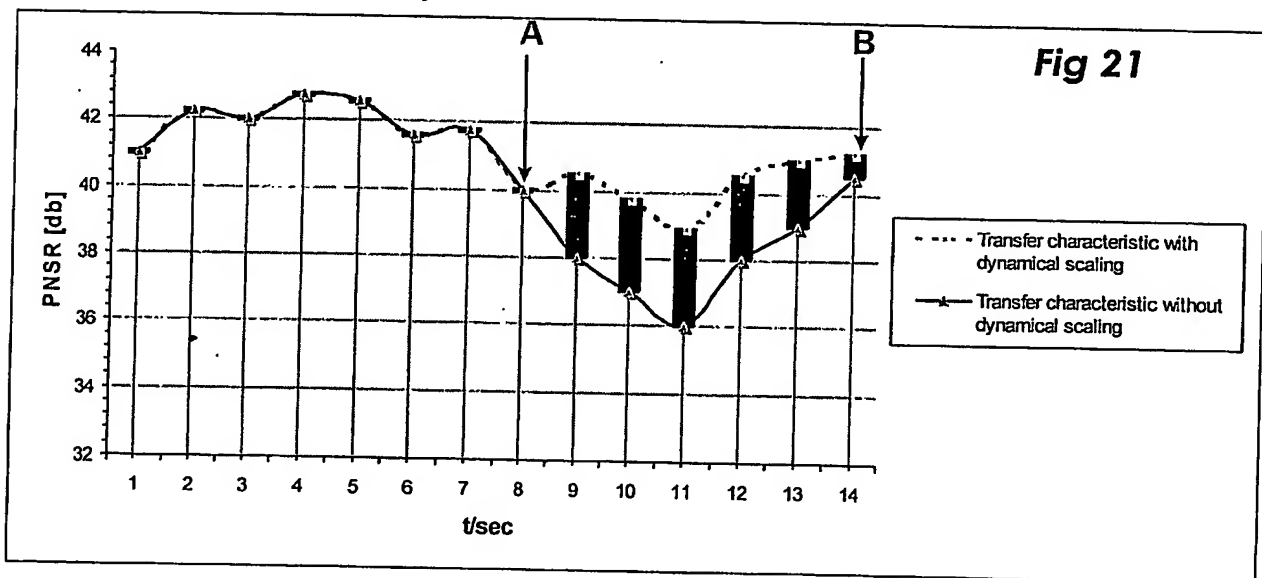
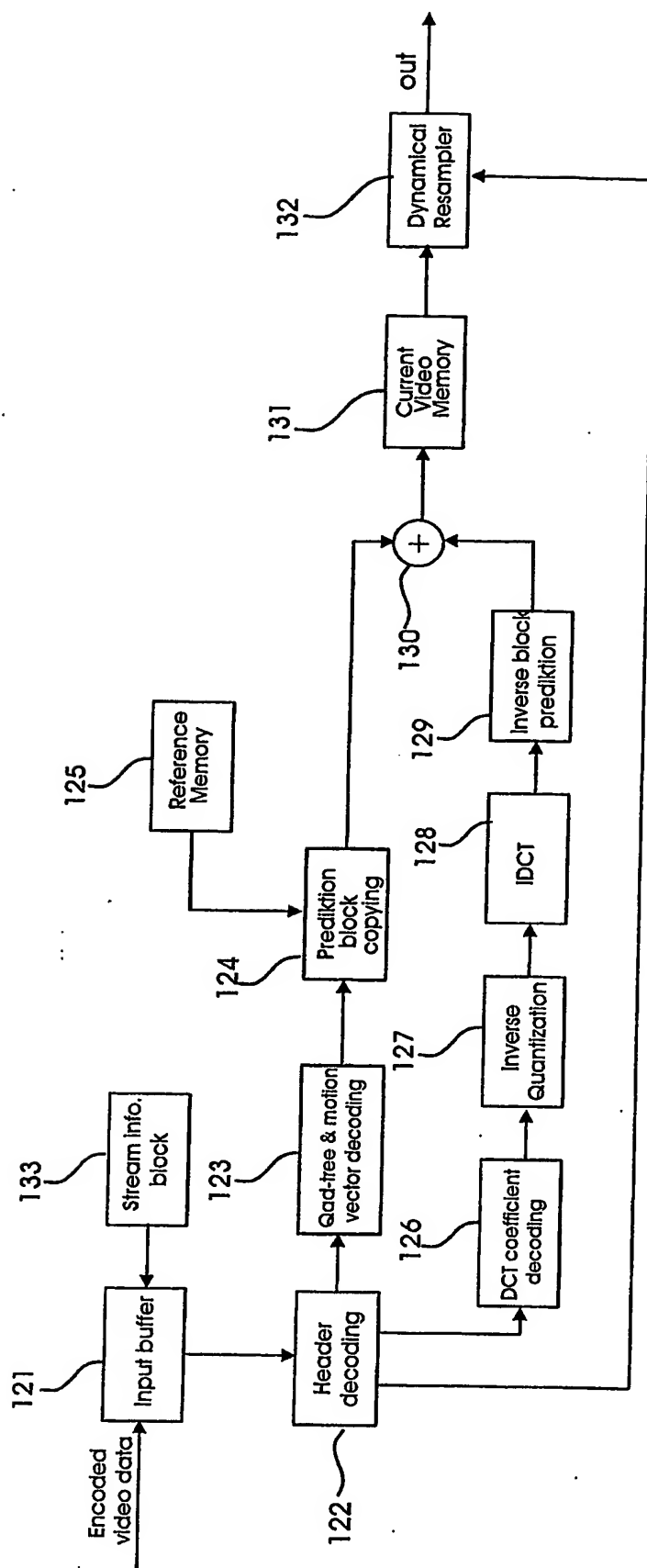


Fig 21

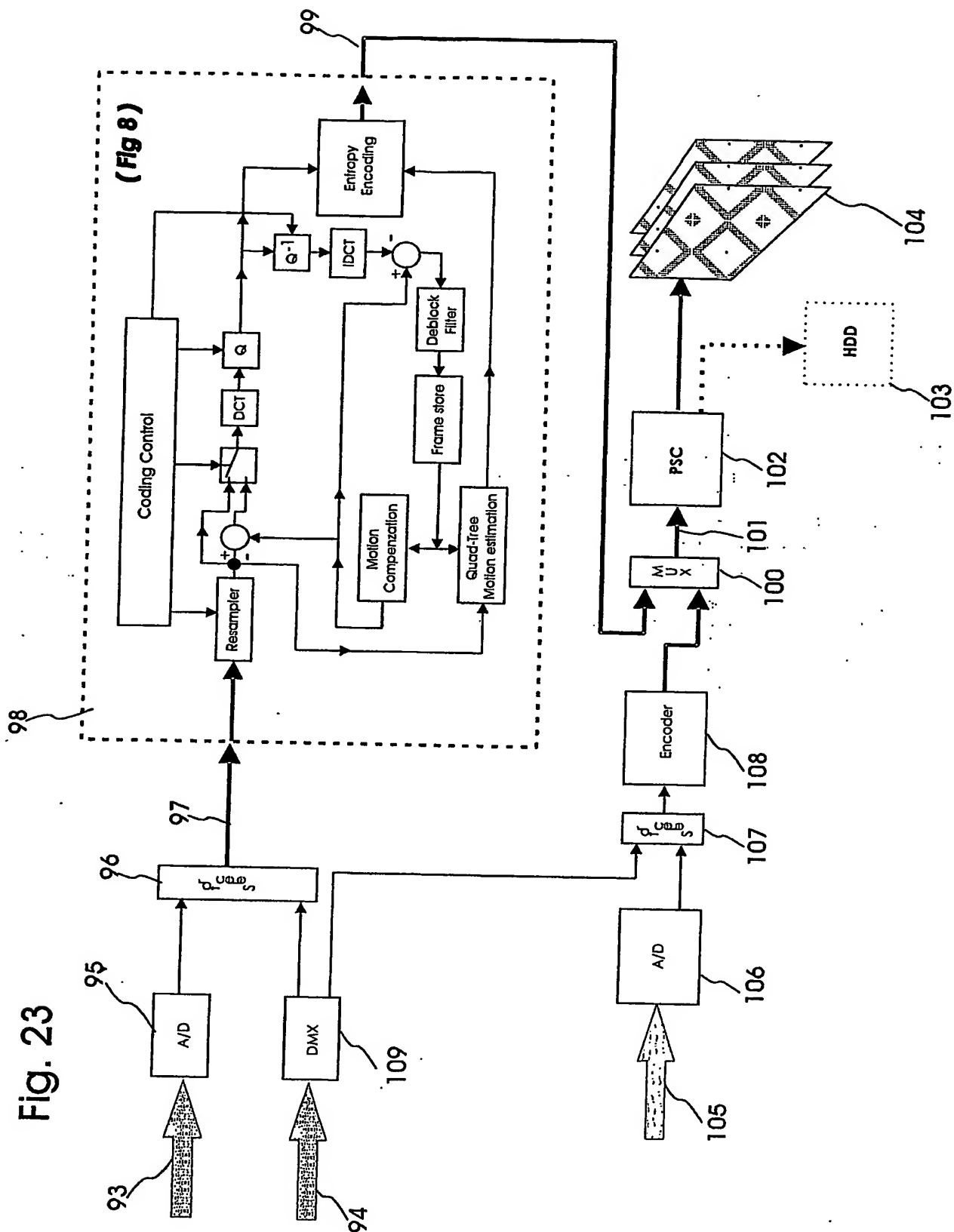
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Fig. 22



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Fig. 23



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